

FAB CONVERGENCE TO 8"/18 μ CMOS TECHNOLOGY

Together with a strategic research institute of the Department of Space, the Government of India signed a contract with Tower Semiconductor (TSL) Ltd as a vendor to supply, install and commission an eight inch (8") wafer FAB and refurbish equipment, with 0.18 CMOS technology.

The fab capacity envisaged is 800-1000 WSPM based on 20-25 mask steps initially, with provision made to scale up later.

Meptagon Management Division (KSI - Kinetic Systems Israel Formerly), has provided TSL with full design and build services.

Design & Engineering scope of work consists of three separate divisions:

- Industrial engineering
- Base-Build design
- Tool-Install design

Mechanical Installation:

- Process & Utility piping
- Electrical & LSS connections
- Cleanroom Architecture
- Fabrication and Quality Approval (vibrations test) of machine pedestals
- Injury free workplace
- QA, QC and Commissioning

Microelectronics Industry

